ABSTRACT OF THE DISCLOSURE

A surface-mount quartz crystal oscillator has a casing having a recess defined therein, a quartz crystal blank fixedly mounted on the bottom of the recess, and an IC (Integrated Circuit) chip having an integrated oscillating circuit connected to the crystal blank. The casing has a frame wall which defines the recess. The IC chip has an outer peripheral edge joined to an upper surface of the frame wall. The crystal blank is hermetically sealed in a space surrounded by the casing and the IC chip. IC connecting terminals are disposed on the upper surface of the frame wall for connection to IC terminals on the IC chip.

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